WHAT IS CLAIMED IS:

1. An electrophoretic display, comprising:a gate line that runs in a first direction; a data line that runs in a second direction; and a pixel electrode formed on an area where the gate line intersects the data line, wherein a portion of the pixel electrode overlaps a portion of the gate line.

2. An electrophoretic display of claim 1, wherein a portion of the pixel electrode overlaps a portion of the data line.

3. An electrophoretic display of claim 1, further comprising: an insulating layer interposed between the data line and the pixel electrode, wherein the insulating layer has a dielectric constant lower than 4.

4. An electrophoretic display of claim 1, wherein the data line is made of metal such as Mo, Mo alloy, Cr, Ta and Ti.

5. An electrophoretic display of claim 1, further comprising: a thin film transistor having a channel; and a source electrode; a drain electrode; wherein the pixel electrode is made of opaque material, and wherein the pixel electrode overlaps the channel of the thin film transistor.

6. An electrophoretic display of claim 3,

wherein the insulating layer is made of a-Si:C:O or a-Si:O:F.

7. An electrophoretic display, comprising;

a gate electrode;

a source electrode;

a drain electrode a semiconductor layer; and

an opaque layer,

wherein the opaque layer lies opposite to the gate electrode with the semiconductor layer disposed therebetween.

8. An electrophoretic display of claim 7, further comprising:

a data line; and

a gate line,

wherein the inclination angle of the gate line or the data line relative to the surface of the substrate ranges between about 20 degrees to about 80 degrees.

9. An electrophoretic display of claim 7, further comprising:

an insulating layer formed between the data line and the pixel electrode,

wherein the insulating layer has a dielectric constant smaller than 4.

10. An electrophoretic display of claim 7,

wherein the data line is made of metal such as Mo, Mo alloy, Cr, Ta and Ti.

11. An electrophoretic display of claim 7, further comprising:

a thin film transistor with a channel;

wherein the pixel electrode is made of opaque material, and

wherein the pixel electrode overlaps the channel of the thin film transistor.

12. An electrophoretic display of claim 9,

wherein the insulating layer is made of a-Si:C:O or a-Si:O:F.

13. An electrophoretic display of claim 7, further comprising:

a pixel electrode;

a data line; and

a gate line,

wherein the pixel electrode overlaps the data line and the gate line.

14. An electrophoretic display, comprising;

a substrate; and

a thin film transistor that comprises

a source electrode and a drain electrode formed on the substrate;

a semiconductor layer formed on the source and the drain electrode;

an insulation layer formed on the semiconductor layer; and

a gate electrode formed on the insulation layer.

15. An electrophoretic display of claim 14, further comprising:

a gate line;

a data line; and

a pixel electrode,

wherein a portion of the pixel electrode overlaps a portion of the gate line, and

wherein a portion of the pixel electrode overlaps a portion of the data line.

16. An electrophoretic display of claim 15,

wherein an insulating layer is between the data line and the pixel electrode, and

wherein the insulating layer has a dielectric constant smaller than 4.

17. An electrophoretic display of claim 15,

wherein the data line is made of metal such as Mo, Mo alloy, Cr, Ta and Ti.

18. An electrophoretic display of claim 15,

wherein the inclination angle of the gate line or the data line relative to the surface of the substrate ranges between about 20 degree to 80 degree.

19. An electrophoretic display of claim 16,

wherein the insulating layer is made of a-Si:C:O or a-Si:O:F.

20. An electrophoretic display, comprising;

a gate line;

a data line;

a pixel electrode;

a common electrode; and

a plurality of micro-capsules,

wherein each of the microcapsules includes electric ink containing a plurality of color pigment particles,

wherein the plurality of color pigment particles are at least one of red, green, blue, cyan, yellow, magneta, blade and white, and

wherein a portion of the pixel electrode overlaps a portion of the gate line.

21. An electrophoretic display of claim 20,

wherein a portion of the pixel electrode overlaps a portion of the data line.

22. An electrophoretic display of claim 20, further comprising:

an insulating layer formed between the data line and the pixel electrode,

wherein the insulating layer has a dielectric constant smaller than 4.